



Sri SAI RAM INSTITUTE OF TECHNOLOGY

An ISO 9001 : 2008 Certified Institution

Sai Leo Nagar, West Tambaram, Chennai - 44. www.sairamit.edu.in

Name : R.KIRUTHIGA

Designation : ASSISTANT PROFESSOR

Email : kiruthiga.eee@sairamit.edu.in

Qualification : M.Tech

Specialization : EEE/ELECTRICAL DRIVES AND CONTROL

Research Interest : POWER SYSTEMS, CONTROL SYSTEMS

Experience in years : **Teaching** UG - 10 years PG **Industry**

No. of Workshop/Conferences/
FDP attended : Workshop 3 Conferences FDP 5

No. of Workshop/Conferences/
FDP Organized : Workshop 1 Conferences NIL FDP NIL



Professional Membership	Life Member, Indian Society for Technical Education (ISTE), New Delhi,
Publications	National :NIL
	International:4
	Book :NIL
Research Funded Projects	NIL
Patents	NIL

Achievements	100% result achieved for the subjects Power System Analysis,Biomedical Instrumentation Best Faculty Award From Dr.Paul's Engineering College. Produced above 95% results in several subjects
Any Other Information	Guided UG projects. Symposium Coordinator of WARROS-2015 Website Coordinator NAAC Criteria 2 Co - ordinator

List of Publications:-

Journal

1. "High Efficient Voltage Regulator Module For Processor Of Desktop Computers" in ' International Journal Of Recent Trends in Engineering,vol.3,no.3,pp 126-130,march 2010
2. "Reduction of Total Harmonics Distortion in PV based Three Level Neutral Point Clamped Inverter using Space Vector Modulation" in International Journal Of Science and Research,Vol.6,Issue 6,June 2017
3. "High Efficient Hybrid Cascaded Multilevel Inverter For Battery Energy Management" in International Journal Of Science and Research,Vol.6,Issue 6,June 2017
4. "Gas Leakage Alerting and Accident Control System Using GMS/GPS Technology" in International Journal Of Science and Research,Vol.6,Issue 6,June 2017

Conferences:

1. "Exploration Of Candidate VRM Topologies For Future Microprocessors" in International Conference On Future Engineering Trends(icfet 2k10) in saveetha university.

2. “High Efficient Voltage Regulator Module For Processor Of Laptop Computers” in International Conference On Power,Control and Embedded Systems Dec 8-10,2010” in College of Engineering, guindy.

List of Patents: NIL

Workshop / Conferences / FDP Attended:-

Workshop:

1. One Day Workshop on “ART OF JOURNAL AND THESIS WRITING” on 19.01.2012 at Sri Sai Ram Engineering College
2. One Day Workshop on “Renewable Energy and Applications” on 19.08.2015 at Agni College of Technology.
3. One day National Level Workshop on “Spectral Analysis using MS0430 for Power Electronics Applications”on 20.02.2016 at VIT Chennai.

FDP:

1. Faculty Development Programme on “Electromagnetic Field Theory & Transmission Lines” on 25th and 26th June 2015 at Sri Sai Ram Institute of Technology.
2. Faculty Development Training Programme Sponsored by Anna University on “EE6008-Microcontroller Based System Design” from 20th to 27th June 2016 at Sri Sai Ram Institute Of Technology.
3. Three Days Faculty Development Programme on “Electromagnetic Theory and Control Systems” from 19th June 2017 to 21st June 2017 at Sree Sastha Institute of Engineering And Technology.
4. Two Days FDP on “Recent Trends On Renewable Energy And Power Electronics Using MATLAB” on 15th and 16th September 2017 at AMET University.

5. Three Days FDP on “Power Electronics for Renewable Energy Systems” at Tagore Engineering College from 18th to 20th June 2018.

Workshop / Conferences / FDP Organized:-

1. Organized Workshop “Hands on Training on Arduino Based System Design” from 04.07.17 to 06.07.17.
2. Organized Guest Lecture “Smart Grid Protection Solution” on 08.08.16
3. Organized - Symposium WARROS-2015- on 29.07.2015